

## Description

# METHOD AND CIRCUIT FOR GENERATING CONSTANT SLEW RATE OUTPUT SIGNAL

### CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This continuation application claims priority from currently pending U.S. Patent Application Serial No. 10/232,090, <sup>*now Patent No. 6,714,462*</sup> entitled "Method and Circuit for Generating Constant Slew Rate Output Signal," filed July 23, 2002, and hereby incorporated herein by reference.

### FIELD OF INVENTION

[0002] The present invention relates, generally, to memory systems. More particularly, the present invention relates to an output buffer, such as may be utilized for memory applications.

### BACKGROUND OF INVENTION

[0003] In the efforts for optimizing and creating new operations in various high-speed microcontroller-based devices,